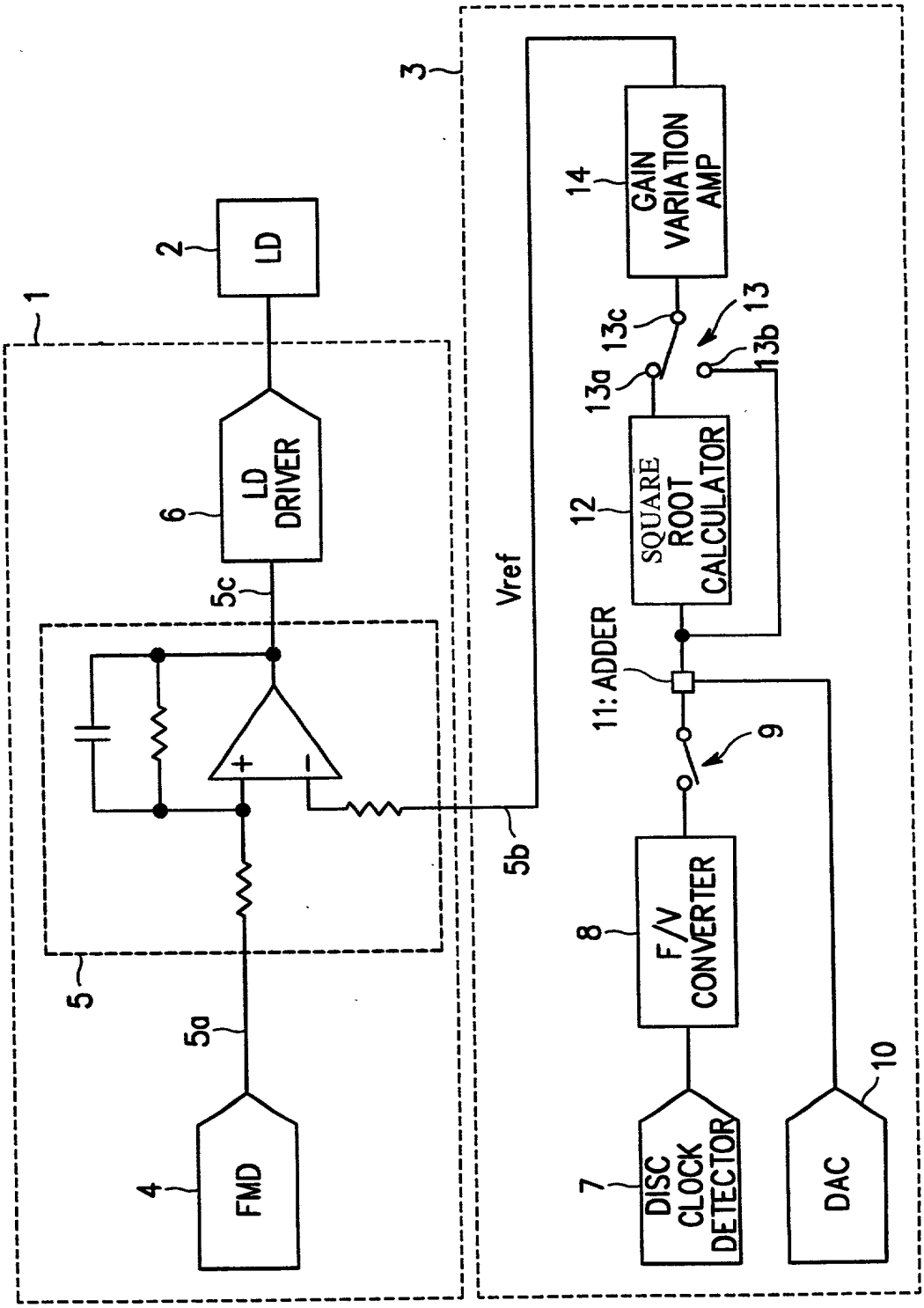
FIG. 1


1
2
LD
3
14
GAIN VARIATION AMP
13c
13
13a
13b
6
LD DRIVER
12
SQUARE ROOT CALCULATOR
5c
Vref
11: ADDER
9
5
5b
8
F/V CONVERTER
5a
4
FMD
7
DISC CLOCK DETECTOR
DAC
10